HMC788A Die Revision, Assembly Site, Marking and Material Change

QUALIFICATION PLAN / STATUS			
TEST	SPECIFICATION	SAMPLE Size	RESULTS
Temperature Cycle (TC)*	JEDEC JESD22-A104	3x25	Pass
High Temperature Storage Life (HTSL)	JEDEC JESD22-A103	1x77	Pass
Solder Heat Resistance (SHR)*	JEDEC/IPC J-STD-020	3x25	Pass
Electrostatic Discharge Human Body Model	ESDA/JEDEC JS-001	3/voltage	Pass <u>+</u> 250V
Electrostatic Discharge Field-Induced Charged Device Model	JEDEC JESD22-C101	3/voltage	Pass <u>+</u> 1000V

HMC788A Qualification Results Summary

* These samples were subjected to preconditioning (per J-STD-020 Level 3) prior to the start of the stress test. Level 3 preconditioning consists of the following: Bake: 24 hrs @ 125°C, Unbiased Soak: 192 hrs @ 30°C, 60%RH, Reflow: 3 passes through an oven with a peak temperature of 260°C.



HMC788A Die Revision, Assembly Site, Marking Code and Material Change



Analog Devices Highly Confidential Information. ©2018 Analog Devices, Inc. All rights reserved.

Detailed Change Description

Commercial Part

Parameter	Current	GoTo
Die rev. – via removal	No	Yes
Assembly Site	Unisem (UG1)	ASE (AEK)
Lead Frame	100Sn	Ni-Pd-Au
Die Attach	Sumitomo CRM 1076DJ conductive	Hitachi EN 4900GC conductive
Mold Compound	Sumitomo G770HCD	Sumitomo G700LYT
Wire diameter (mils)	1.0	0.8
Marking Code	788A	Y7H



Analog Devices Highly Confidential Information. ©2019 Analog Devices, Inc. All rights reserved.